Embedded System Design and Synthesis

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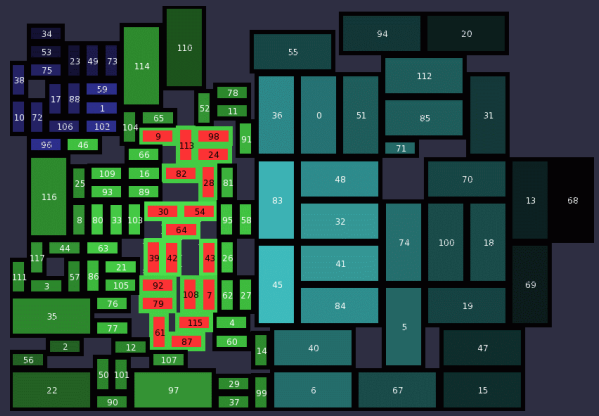

Outline

1. Single-electron tunneling transistors

2. Memory expansion for MMU-less embedded systems

3. Power deregulation: eliminating voltage regulators from multicore embedded systems

4. Homework

SET talk

Outline

1. Single-electron tunneling transistors
2. Memory expansion for MMU-less embedded systems
3. Power deregulation: eliminating voltage regulators from multicore embedded systems
4. Homework

MEMMU talk

Outline

1. Single-electron tunneling transistors
2. Memory expansion for MMU-less embedded systems
3. Power deregulation: eliminating voltage regulators from multicore embedded systems
4. Homework

Deregulation talk

Outline

1. Single-electron tunneling transistors
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Project presentation

5–10 minutes per person

1. Motivation
2. Problem definition
3. Proposed solution
4. Method of evaluating solution

Project report

- Due 6 September by email
- Can give to me in person on 5 September

1. Motivation
2. Problem definition
3. Proposed solution
4. Method of evaluating solution
5. Evaluation results

Review

- Review the lecture notes
- Review your notes taken when I used the whiteboard instead of slides
- Review the research articles you have read